

**SHARP**

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TECHNICAL LITERATURE  
FOR  
TFT-LCD module

MODEL No. LQ043T3DX02

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PRESENTED

*T. Maki*

T. MAKI I  
DEPARTMENT GENERAL MANAGER  
ENGINEERING DEPARTMENT II  
MOBILE LCD DESIGN CENTER II  
MOBILE LIQUID CRYSTAL DISPLAY GROUP  
SHARP CORPORATION



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### 1. Applicable Scope

This technical literature is applicable to TFT-LCD Module “LQ043T3DX02” only.

### 2. General Description

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, Input FPC and a back light unit. Graphics and texts can be displayed on a  $480 \times 3 \times 272$  dots panel with about 16 million colors by supplying 24-bit data signals (8-bit  $\times$  RGB), Four timing signals, logic (typ. +2.5V), analog (typ. +5V) supply voltages for TFT-LCD panel driving and supply voltage for back light.

### 3. Mechanical (Physical) Specifications

Item	Specifications	Unit
Screen size	10.9 (4.3" type) diagonal	cm
Active area	95.04(H) $\times$ 53.856(V)	mm
Pixel format	480 $\times$ 272	pixel
	1Pixel = R+G+B dots	
Pixel pitch	0.198(H) $\times$ 0.198(V)	mm
Pixel configuration	R,G,B vertical stripes	
Display mode	Normally black	
Unit outline dimensions	105.5(W) $\times$ 67.2(H) $\times$ 3.95(D)	mm
Mass	50	g
Polarizer $\cdot$ Surface treatment	Clear hard coat	

※ The above-mentioned table indicates module sizes without some projections and FPC.

## 4. Input Terminal Names and Functions

## 4-1. TFT LCD Panel Driving (Reference Connector :

Hirose Electric CO., LTD. Product No.: FH12A-40S-0.5SH(55) Top contact type)

※ Bottom contact type connectors are also prepared. Ex: FH12-40S-0.5SH(55)

※ The FPCs of this LCD module are gold plated, so the connector contacts should be also gold plated.

Terminal No.	Terminal name	Function	Remarks
1	GND	GND(0V)	
2	GND	GND(0V)	
3	VCC	+2.5V power source	
4	VCC	+2.5V power source	
5	R0	RED Data Signal (LSB)	
6	R1	RED Data Signal	
7	R2	RED Data Signal	
8	R3	RED Data Signal	
9	R4	RED Data Signal	
10	R5	RED Data Signal	
11	R6	RED Data Signal	
12	R7	RED Data Signal (MSB)	
13	G0	GREEN Data Signal (LSB)	
14	G1	GREEN Data Signal	
15	G2	GREEN Data Signal	
16	G3	GREEN Data Signal	
17	G4	GREEN Data Signal	
18	G5	GREEN Data Signal	
19	G6	GREEN Data Signal	
20	G7	GREEN Data Signal (MSB)	
21	B0	BLUE Data Signal (LSB)	
22	B1	BLUE Data Signal	
23	B2	BLUE Data Signal	
24	B3	BLUE Data Signal	
25	B4	BLUE Data Signal	
26	B5	BLUE Data Signal	
27	B6	BLUE Data Signal	
28	B7	BLUE Data Signal (MSB)	
29	GND	GND(0V)	
30	CK	Clock signal to sample each date	
31	DISP	Display ON/OFF Signal	
32	Hsync	Horizontal synchronizing signal	
33	Vsync	Vertical synchronizing signal	
34	NC	NC	Note 1
35	AVDD	+5V Analog power source	
36	AVDD	+5V Analog power source	
37	NC	NC	Note 1
38	TEST1	TEST1	Note 2
39	TEST2	TEST2	Note 3
40	TEST3	TEST3	Note 3

Note 1) They have been open within FPC.

Note 2) Please be sure to set 38 pins (TEST1) to open.

Note 3) Please be sure to connect 39 pin (TEST2) ,40 pin (TEST3) with GND.

4-2. Backlight

0.5mmP 4Pin FPC (Reference Connector :

Kyocera Elco Corporation Product No. : 6298 Bottom contact type)

※ Top contact type connectors are also prepared.

※ The FPCs of this LCD module are gold plated, so the connector contacts should be also gold plated.

Terminal No.	Signal	Function
1	V <sub>LED-</sub>	LED Power Source Input terminal (Cathode side)
2	NC	No Connection
3	NC	No Connection
4	V <sub>LED+</sub>	LED Power Source Input terminal (Anode side)

5. Absolute Maximum Ratings

Item	Symbol	Conditions	Rated value	Unit	Remarks
Input voltage	V <sub>1</sub>	Ta=25°C	-0.3 ~ VCC+0.3	V	【Note 1】
2.5 V Power supply voltage	VCC	Ta=25°C	0 ~ +4.5	V	
5 V Power supply voltage	AVDD	Ta=25°C	0 ~ +6.0	V	
Temperature for storage	T <sub>stg</sub>	—	-25 ~ +60	°C	【Note 2】
Temperature for operation	T <sub>opa</sub>	—	-10 ~ +50	°C	【Note 3】
LED Input electric current	I <sub>LED</sub>	Ta=25°C	25	mA	【Note 4】
LED electricity consumption	P <sub>LED</sub>	Ta=25°C	100	mW	【Note 4】

【Note 1】 CK,R0~R7,G0~G7,B0~B7,Hsync,Vsync,DISP

【Note 2】 Humidity : 80%RHMax. (Ta≤40°C)

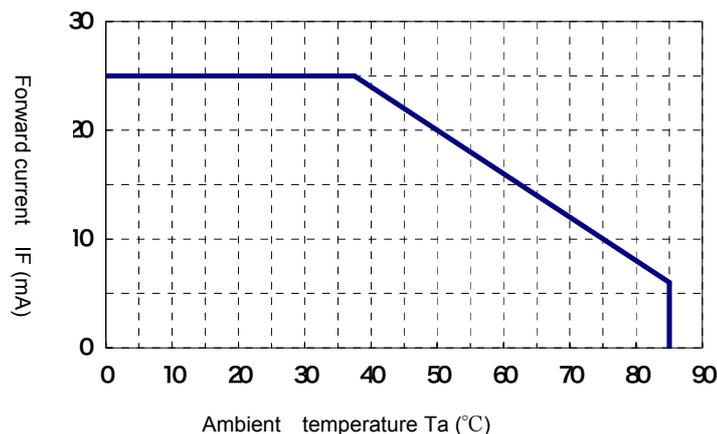
Maximum bulb temperature under 39°C (Ta>40°C) See to it that no dew will be condensed.

【Note 3】 Panel surface temperature prescribes.

(Reliability is examined at ambient temperature of 50°C.)

【Note 4】 Power consumption of one LED (Ta=25°C) (use LED NESW008B 7pieces)

Ambient temperature and the maximum input are fulfilling the following operating conditions.



Ambient temperature and the maximum input

6. Electrical Characteristics

6-1. TFT LCD Panel Driving

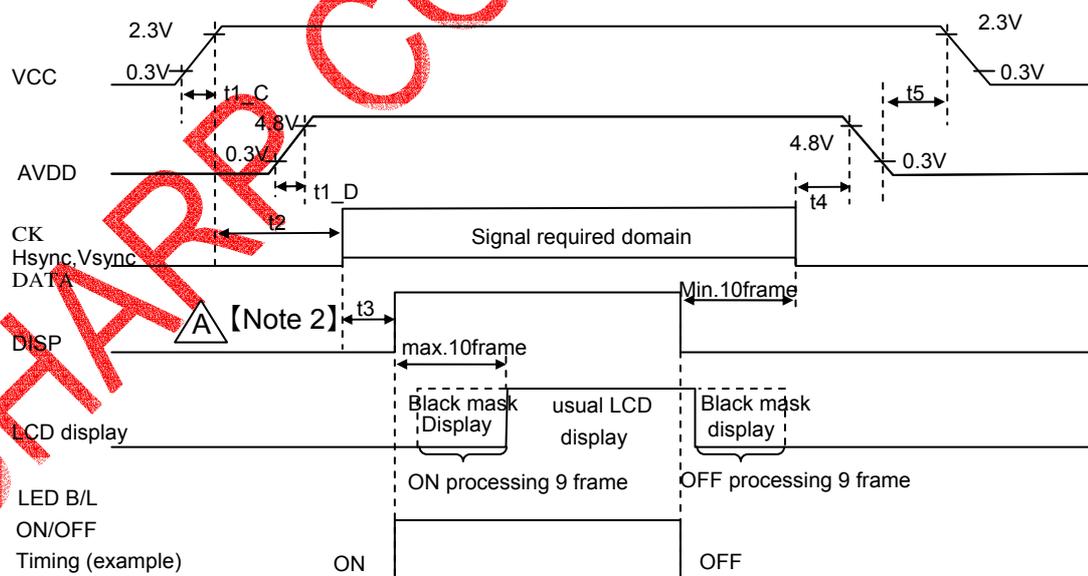
Ta = 25°C

Item	Symbol	Min.	Typ.	Max.	Unit	Remarks	
+2.5V power supply	DC voltage	VCC	+2.3	+2.5	+3.3	V	【Note 1】
	DC Current	ICC		1.2	3	mA	【Note 3】
+5V power supply	DC voltage	AVDD	+4.8	+5.0	+5.2	V	【Note 1】
	DC Current	I <sub>AVDD</sub>		10	18	mA	【Note 3】
Permissive Input ripple voltage	V <sub>RFVCC</sub>			100	mVp-p	V <sub>CC</sub> =+2.5V	
	V <sub>RFAVDD</sub>			100	mVp-p	V <sub>CC</sub> =+5.0V	
Input voltage (Low)	V <sub>IL</sub>			0.2 V <sub>CC</sub>	V	【Note 4】	
Input voltage (High)	V <sub>IH</sub>	0.8 V <sub>CC</sub>			V	【Note 4】	
Input current (Low)	I <sub>OL</sub>			4.0	μA	V <sub>I</sub> =0V 【Note 4】	
Input current (High)	I <sub>OH</sub>			4.0	μA	V <sub>I</sub> =2.5V 【Note 4】	

※ The rush current will flows when power supply is turned on, so please design the power supply circuit referring to 【Note 5】  
(The rush current changes according to the condition of the supply voltage value, rising time and so on.)

【Note 1】

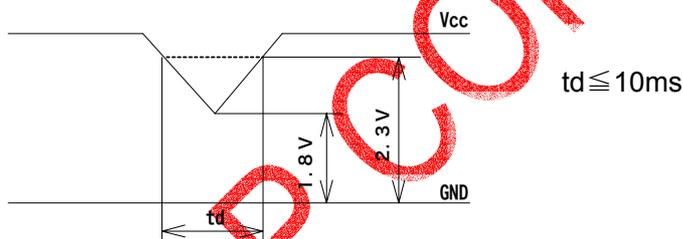
Sequences of supply voltage and signals



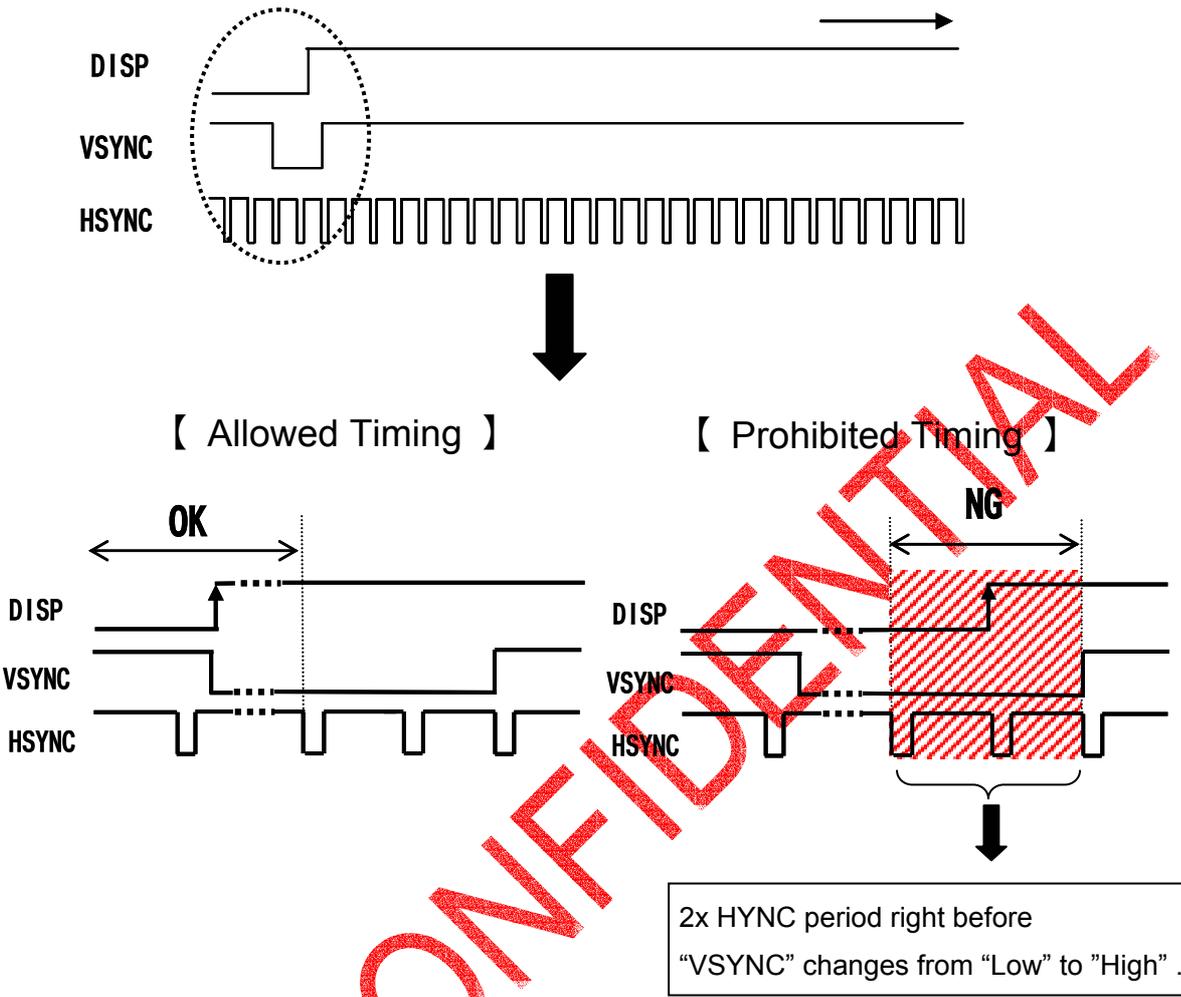
- Please do not supply AVDD before VCC.
- It discharges and boost up voltage for TFT module on the basis of a DISP-signal  
It drives Max-10 frames (about 0.2seconds) from change of DISP-signals by reasons that  
It takes time for 9 frames while each processing operation.  
Therefore, the display start is delayed for 10 frames and Ten or more frames needs to be  
voltage maintained at the time of a display end.
- It is not problem to set up DISP=L , ANDD=GND when VCC voltage is supplied
- Please don't set various signals to Hi-Z when VCC-voltage is supplied  
in reason that those signals are CMOS input.
- Don't change DISP signal into the state of H level When AVDD voltage is in the state of GND.
- The ON/OFF timing of LED Back Light is an example.

	MIN	TYP	MAX	unit	Remarks
t1_C	0	—	10	ms	
t1_D	0.5	—	10	ms	
t2	50	—	—	ms	
t3	0	—	—	ms	【Note 2】 
t4	0	—	—	ms	
t5	0	—	—	ms	

Dip Conditions for supply voltage



△【Note 2】 While “VSYNC” is “Low”, don't change “DISP” signal “Low” to “High”.



【Note 3】 Typical current situation: 256-gray bar pattern VCC=2.5V AVDD=5.0V



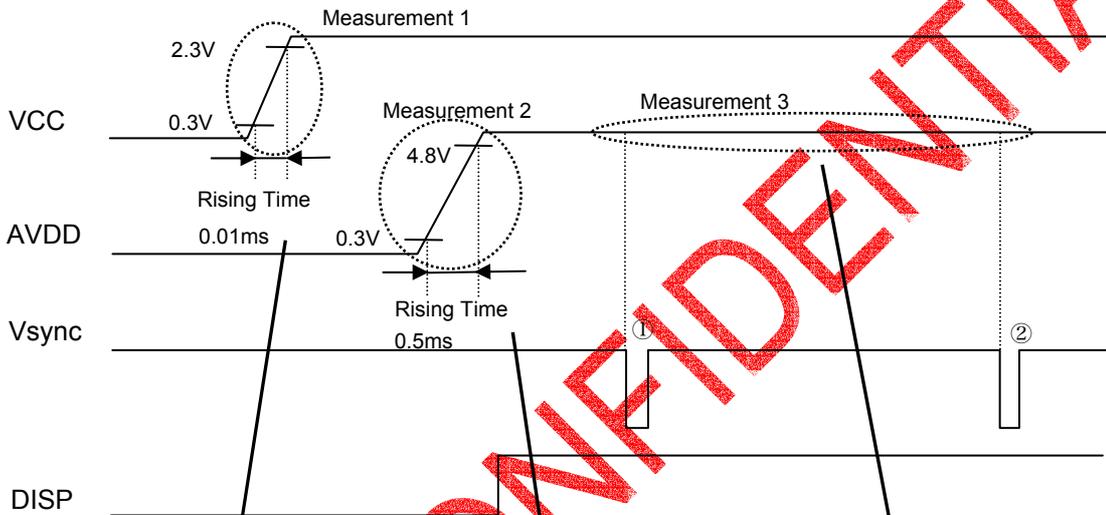
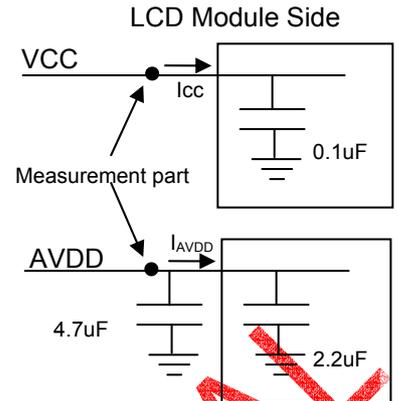
【Note 4】 CK, R0~R7, G0~G7, B0~B7, Hsync, Vsync, DISP

【 Note 5 】

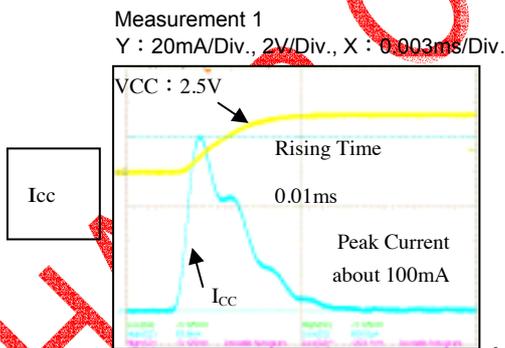
An example of rush current measurement

◎Measurement conditions

- Power supply voltage VCC : 2.5V AVDD : 5.0V
- Disp signal : OFF ⇒ ON
- Other input signals : GND
- Measurement system : refer to right Fig.
- rush current measurement timing : refer to following Fig.

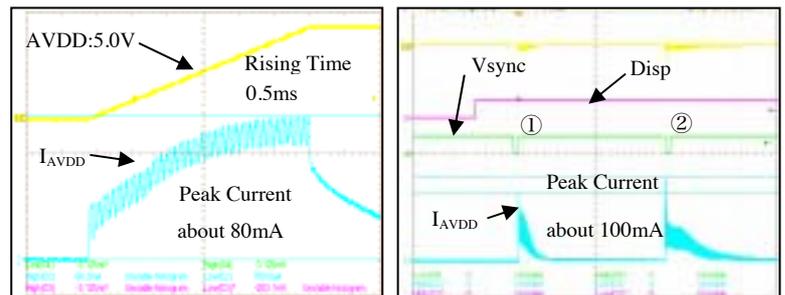


◎Measurement Example



IAVDD

Measurement 2 Y : 20mA/Div., 2V/Div., X : 0.08ms/Div. Measurement 3 Y : 50mA/Div., 5V/Div., X : 4ms/Div.



These rush current won't flow stationary, these will flow at the timing shown in Measurement 3.

## 6-2. Back light driving

The back light system has seven LEDs

[NESW008B]

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Rated Voltage	$V_{BL}$	—	—	29.4	V	
Rated Current	$I_L$	—	18	20	mA	Ta=25°C

## 7. Timing characteristics of input signals

An input signal timing waveform is shown in Fig. 2.

## 7-1 Timing characteristics

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
Clock	Frequency	1/Tc	7.83	9.00	9.26	MHz	
	Duty ratio	Th/T	40	50	60	%	
Data	Set up time	Tds	25	—	—	ns	
	Hold time	Tdh	25	—	—	ns	
Horizontal synchronizing	Period	TH	—	525	—	Clock	
	Pulse width	THp	—	41	—	Clock	
	Horizontal period	THd	—	480	—	Clock	
	Back porch	THb	—	2	—	Clock	
	Front porch	THf	—	2	—	Clock	
Vertical synchronizing	Period	TV	—	286	—	Line	
	Pulse width	TVp	—	10	—	Line	
	Vertical period	TVd	—	272	—	Clock	
	Back porch	TVb	—	2	—	Line	
	Front porch	TVf	—	2	—	Line	

- 【Note】
- In case of using the slow frequency, the deterioration of display, flicker etc may occur.
  - The timing characteristics are basically fixed as above.

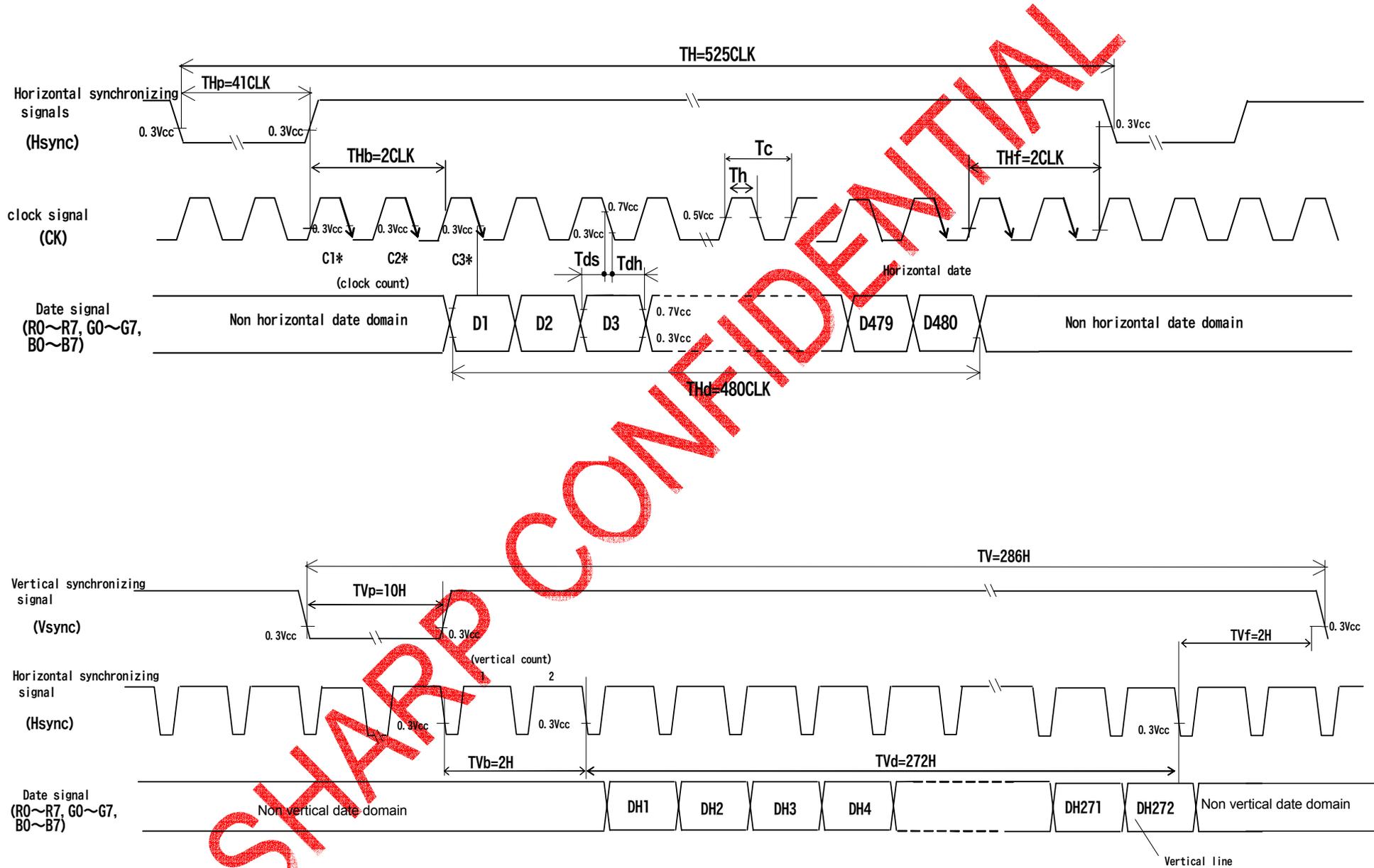
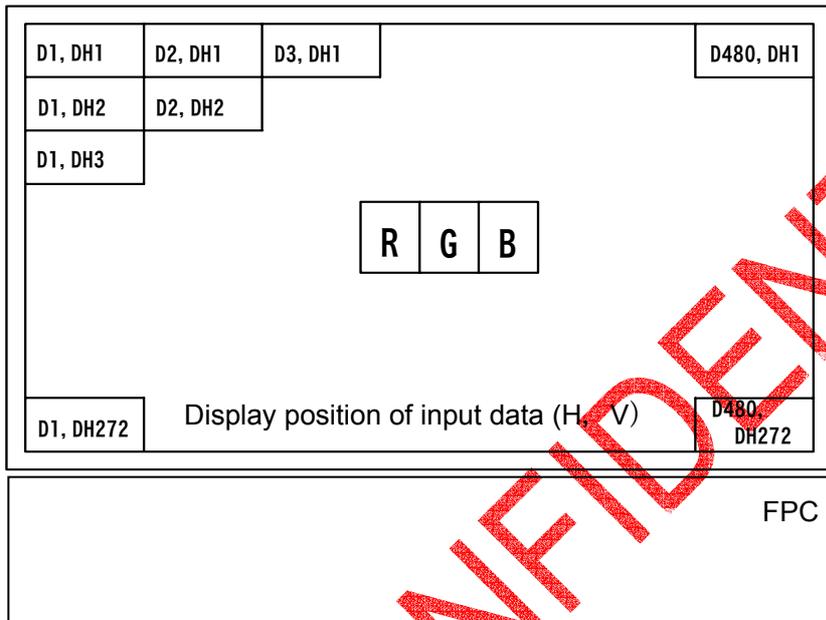
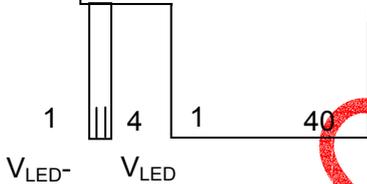


Fig 2 Input signal timing

7-3 Input Data Signals and Display Position on the screen



Please refer to 4-1 (P6) about Pin arrangement.



Please refer to 4-2 (P7) about LED side Pin arrangement.

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9. Optical Characteristics

Module characteristics

Ta = 25°C, VCC = +2.5V, AVDD = 5.0V

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	
Viewing angle range	Horizontal	CR > 10	—	80	—	Deg.	【Note1,4】	
	Vertical		θ11	—	80	—		Deg.
			θ12	—	80	—		Deg.
Contrast ratio	CR	θ=0°	100	400	—		【Note2,4】	
Response Time	Rise	θ=0°	—	30	45	ms	【Note3,4】	
	Decay		—	30	45	ms		
Chromaticity of White	x		0.246	0.296	0.346		【Note4】	
	y		0.293	0.343	0.393			
Luminance of white	XL1		—	165	—	cd/m <sup>2</sup>	ILED=18mA 【Note4】	

※ The optical characteristics measurements are operated under a stable luminescence(I LED = 18mA) and a dark condition. (refer to Fig.3)

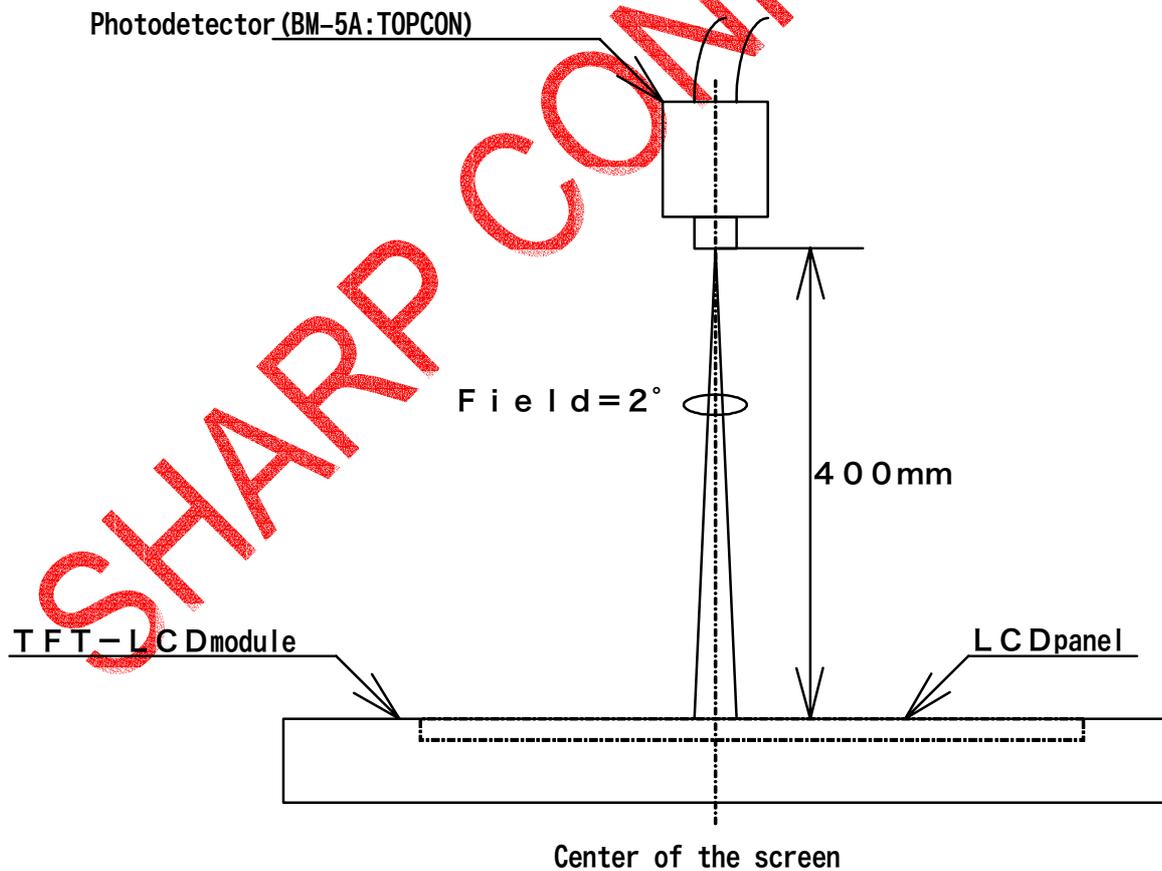
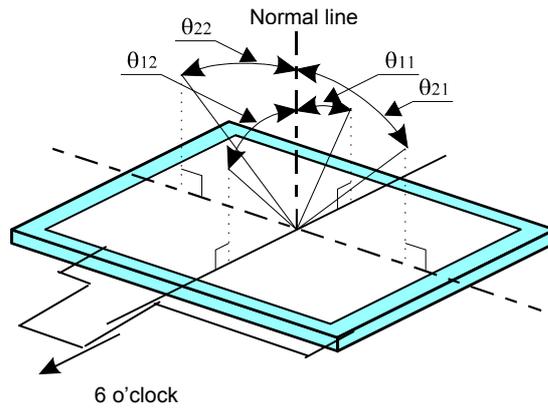


Fig.3 Optical characteristics measurement method

【 Note 1 】 Definitions of viewing angle range



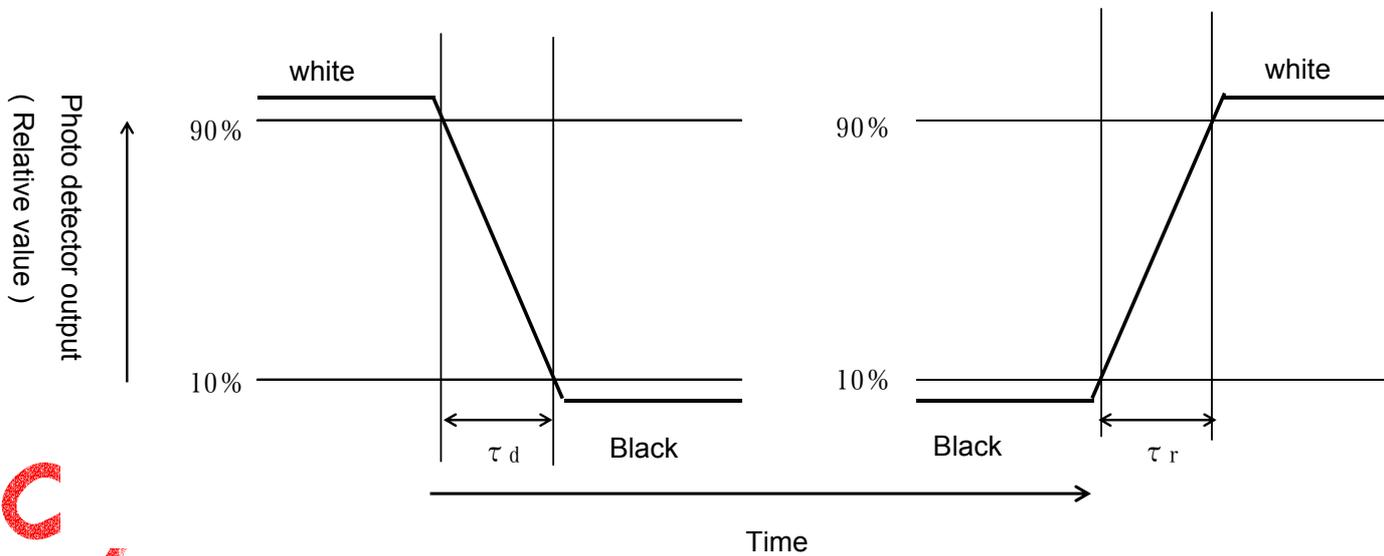
【 Note 2 】 Definition of contrast ratio

The contrast ratio is defined as the following

$$\text{Contrast ratio(CR)} = \frac{\text{Luminance (brightness) with all pixels white}}{\text{Luminance (brightness) with all pixels black}}$$

【 Note 3 】 Definition of response time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white"



【 Note 4 】 This shall be measured at center of the screen.

## 10. Handling of modules

### 10-1 Inserting the FPC into its connector and pulling it out.

- ① Be sure to turn off the power supply and the signals when inserting or disconnecting the cable.
- ② Please insert for too much stress not to join FPC in the case of insertion of FPC.

### 10-2 About handling of FPC

- ① The bending radius of the FPC should be more than 1.4mm, and it should be bent evenly.
- ② Do not dangle the LCD module by holding the FPC, or do not give any stress to it.

### 10-3 Mounting of the module

- ① The module should be held on to the plain surface. Do not give any warping or twisting stress to the module.
- ② Please consider that GND can ground a modular metal portion etc. so that static electricity is not charged to a module.

### 10-4 Cautions in assembly / Handling pre cautions.

As the polarizer can be easily scratched, be most careful in handling it.

- ① Work environments in assembly.

Working under the following environments is desirable:

- a) Implement more than  $1M\Omega$  conductive treatment ( by placing a conductive mat or applying Conductive paint ) on the floor or tiles.
  - b) No dusts come in to the working room. Place an adhesive, anti-dust mat at the entrance of the room.
  - c) Humidity of 50~70% and temperature of 15~27°C are desirable.
  - d) All workers wear conductive shoes, conductive clothes, conductive fingerstalls and grounding belts without fail.
  - e) Use a blower for electrostatic removal. Set it in a direction slightly tilt downward so that each Module can be well subjected to its wind. Set the blower at an optimum distance between the blower and the module.
- ② How the remove dust on the polarizer
    - a) Blow out dust by the use of an N2 blower with antistatic measures taken. Use of an ionized air Gun is recommendable.
    - b) When the panel surface is soiled, wipe it with soft cloth.
  - ③ In the case of the module's metal part (shield case) is stained, wipe it with a piece of dry, soft cloth. If rather difficult, give a breath on the metal part to clean better.
  - ④ If a water dropped, etc. remains stuck on the polarizer for a long time, it is apt to get discolored or cause stains. Wipe it immediately.
  - ⑤ As a glass substrate is used for the TFT-LCD panel, if it is dropped on the floor or hit by something hard, it may be broken or chipped off.
  - ⑥ Since CMOS LSI is used in this module, take care of static electricity and take the human earth into consideration when handling.

## 10-5 Others

- ① Regarding storage of LCD modules, avoid storing them at direct sunlight-situation.

You are requested to store under the following conditions:

(Environmental conditions of temperature/humidity for storage)

- (1) Temperature: 0~40°C

- (2) Relative humidity : 95% or less

- As average values of environments (temperature and humidity) for storing, use the following control guidelines:

Summer season: 20~35°C, 85% or less Winter season: 5~15°C, 85% or less

- If stored under the conditions of 40°C and 95% RH, cumulative time of storage must be less than 240 hours.
- ② If stored at temperatures below the rated values, the inner liquid crystal may freeze, causing cell destruction. At temperatures exceeding the rated values for storage, the liquid crystal may become isotropic liquid, making it no longer possible to come back to its original state in some cases.
- ③ If the LCD is broken, do not drink liquid crystal in the mouth. If the liquid crystal adheres to a hand or foot or to clothes, immediately cleanse it with soap.
- ④ If a water drop or dust adheres to the polarizer, it is apt to cause deterioration. Wipe it immediately.
- ⑤ Be sure to observe other caution items for ordinary electronic parts and components.

## 11. Reliability test items

No.	Test item	Conditions
1	High temperature storage test	Ta = 60°C 240h
2	Low temperature storage test	Ta = -25°C 240h
3	High temperature & high humidity operation test	Ta = 40°C ; 95%RH 240h (No condensation)
4	High temperature operation test	Ta = 50°C 240h (The panel temp. must be less than 50°C)
5	Low temperature operation test	Ta = -10°C 240h
6	Vibration test (non- operating)	Frequency : 10~55Hz/Vibration width (one side) : 1.5mm Sweep time : 1minutes Test period : (2 hours for each direction of X,Y,Z)
7	Shock test	Direction : ±X, ±Y, ±Z, Time : Third for each direction. Impact value : 100G Action time 6ms
8	Thermal shock test	Ta=-25°C~60°C /10 cycles (30 min) (30min)

## 【Result Evaluation Criteria】

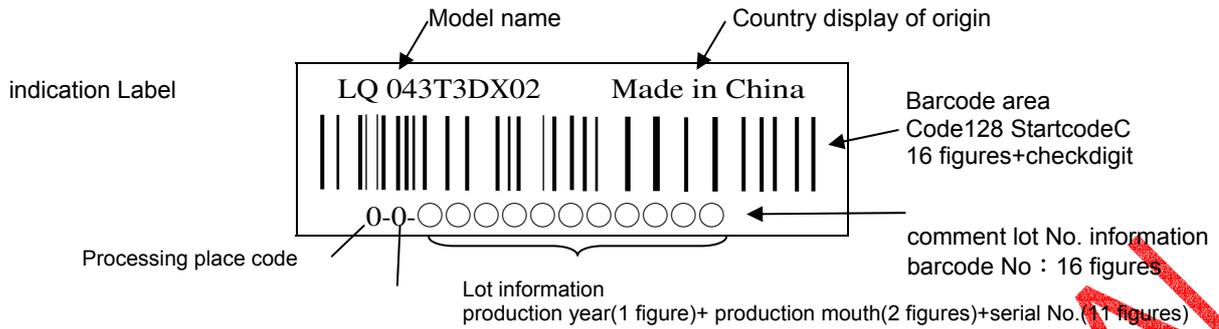
Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

## 12. Display Grade

The standard regarding the grade of color LCD displaying modules should be based on the delivery inspection standard.

13. Lot No. marking

The lot No. will be indicated on individual labels. The location is as shown



outward form : width  $29.0 \pm 0.5\text{mm}$  length  $11.5 \pm 0.5\text{mm}$

comment column :

- 1 figure Processing place code 「0」 WSEC Wuxi Factory  
「1」 WSEC Lianyungang Factory
- 2 figures Change history administration No. 「0」
- 3 figures Production year
- 4, 5 figures Production month
- 6 -16 figures Serial No.

14. Others

- 1 Disassembling the module can cause permanent damage and you should be strictly avoided.
- 2 Please be careful that you don't keep the screen displayed fixed pattern image for a long time, since retention may occur.
- 3 If you pressed down a liquid crystal display screen with your finger and so on, the alignment disorder of liquid crystal will occur. And then It will become display fault.  
Therefore, Be careful not to touch the screen directly, and to consider not stressing to it.
- 4 In the circuit parts on FPC, We don't change LCD module parts without announces.

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